



2123

Patent
706316-1203

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Beausoleil, W.F. et al.

Application No.: 09/655,596

Filed: September 6, 2000

For: High Speed Software Driven Emulator
Comprised Of A Plurality of Emulation
Processors With A Method To Allow Memory
Read/Writes Without Interrupting The
Emulation

Group Art Unit: 2123

Examiner: Not Yet Assigned

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Technology Center 2100

TRANSMITTAL LETTER

Assistant Commissioner
For Patents
Washington, D.C. 20231

Sir:

Enclosed for filing in the above-identified application is the following:

1. **DOCUMENTS ENCLOSED:**

- ☒ Revocation of Power of Attorney or Authorization of Agent
- ☒ Power of Attorney or Authorization of Agent
- ☒ Statement Under 37 CFR 3.73(b) with Exhibits 1 and 2 attached

CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

October 17, 2002
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Signature of Person Mailing Paper

☒ Although applicant believes no fee is required, the Commissioner is authorized to charge
Deposit Account No. 150665 for any fees that may be associated with this communication.

Respectfully submitted,

LYON & LYON LLP

Dated: October 17, 2002

By: 

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